

## SFP 2.67G 850nm 300M Duplex LC

SLS-2585-S3-X




### Overview

The SFP transceivers are high performance, cost effective modules supporting dual data-rate of 2.67Gbps and 300m transmission distance with MMF.

The transceiver consists of three sections: a VCSEL laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The SFP transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.

### Features

- ◆ Up to 2.67Gb/s bi-directional data links
- ◆ 850nm VCSEL and PIN photo detector
- ◆ Compliant with SFP MSA and SFF-8472 with duplex LC
- ◆ Digital Diagnostic Monitoring:  
Internal Calibration or External Calibration
- ◆ 300m transmission with 50/125µm MMF
- ◆ Compatible with RoHS 
- ◆ +3.3V single power supply
- ◆ Operating case temperature:  
Standard: 0 to +70°C  
Industrial: -40 to +85°C

### Applications

- ◆ SDH STM-16 and SONET OC-48 system
- ◆ 2X Fiber Channel
- ◆ Switch to Switch interface
- ◆ Switched backplane applications
- ◆ Router/Server interface
- ◆ Other optical transmission systems

### Ordering Information

| Part Number    | Product Description  |
|----------------|--|
| SLS-2585-S3    | SFP 2.67Gbps, 850nm, 300m, 0°C ~ +70°C                                       |
| SLS-2585-S3-D  | SFP 2.67Gbps, 850nm, 300m, 0°C ~ +70°C, With Digital Diagnostic Monitoring   |
| SLS-2585-S3T   | SFP 2.67Gbps, 850nm, 300m, -40°C ~ +85°C                                     |
| SLS-2585-S3T-D | SFP 2.67Gbps, 850nm, 300m, -40°C ~ +85°C, With Digital Diagnostic Monitoring |

### Module Block Diagram

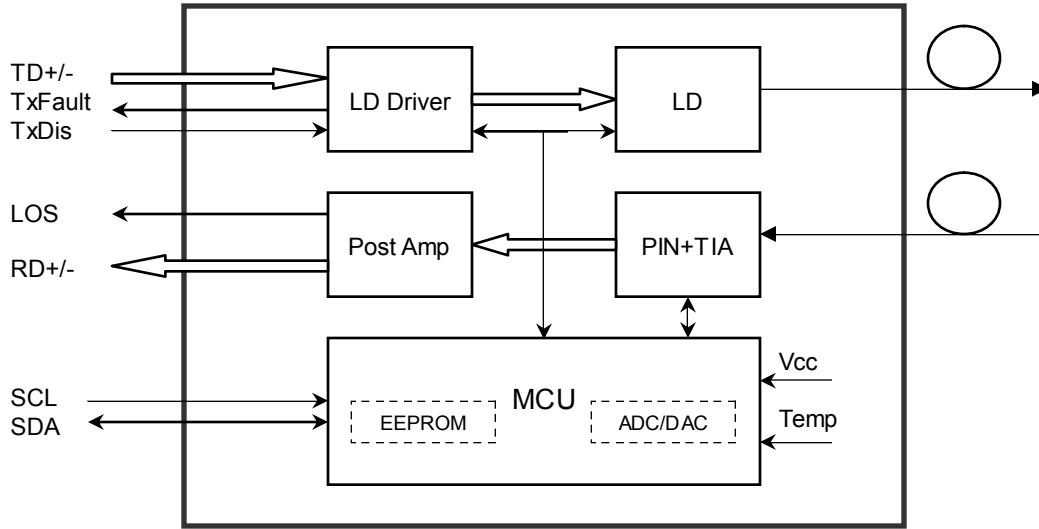


Figure1. Block Diagram

### Absolute Maximum Ratings

| Parameter           | Symbol | Min  | Max | Unit |
|---------------------|--------|------|-----|------|
| Supply Voltage      | Vcc    | -0.5 | 4.5 | V    |
| Storage Temperature | Ts     | -40  | +85 | °C   |
| Operating Humidity  | -      | 5    | 85  | %    |

### Recommended Operating Conditions

| Parameter                  | Symbol     | Min  | Typical | Max  | Unit |
|----------------------------|------------|------|---------|------|------|
| Operating Case Temperature | Standard   | 0    |         | +70  | °C   |
|                            | Industrial | -40  |         | +85  | °C   |
| Power Supply Voltage       | Vcc        | 3.13 | 3.3     | 3.47 | V    |
| Power Supply Current       | Icc        |      |         | 300  | mA   |
| Data Rate                  |            |      | 2.67    |      | Gbps |

### Optical and Electrical Characteristics

| Parameter            | Symbol      | Min | Typical | Max  | Unit | Notes |
|----------------------|-------------|-----|---------|------|------|-------|
| <b>Transmitter</b>   |             |     |         |      |      |       |
| Centre Wavelength    | $\lambda_c$ | 830 | 850     | 860  | nm   |       |
| Spectral Width (RMS) | $\sigma$    |     |         | 0.85 | nm   |       |
| Average Output Power | Pout        | -10 |         | -3   | dBm  | 1     |

**Datasheet**

|                                  |             |     |     |      |          |   |
|----------------------------------|-------------|-----|-----|------|----------|---|
| Extinction Ratio                 | ER          | 9   |     |      | dB       |   |
| Optical Rise/Fall Time (20%~80%) | $t_r/t_f$   |     |     | 0.16 | ns       |   |
| Data Input Swing Differential    | $V_{IN}$    | 400 |     | 1800 | mV       | 2 |
| Input Differential Impedance     | $Z_{IN}$    | 90  | 100 | 110  | $\Omega$ |   |
| TX Disable                       | Disable     |     | 2.0 |      | Vcc      | V |
|                                  | Enable      |     | 0   |      | 0.8      | V |
| TX Fault                         | Fault       |     | 2.0 |      | Vcc      | V |
|                                  | Normal      |     | 0   |      | 0.8      | V |
| <b>Receiver</b>                  |             |     |     |      |          |   |
| Centre Wavelength                | $\lambda_c$ | 770 |     | 860  | nm       |   |
| Receiver Sensitivity             |             |     |     | -18  | dBm      | 3 |
| Receiver Overload                |             | -3  |     |      | dBm      | 3 |
| LOS De-Assert                    | $LOS_D$     |     |     | -20  | dBm      |   |
| LOS Assert                       | $LOS_A$     | -30 |     |      | dBm      |   |
| LOS Hysteresis                   |             | 1   |     | 4    | dB       |   |
| Data Output Swing Differential   | $V_{out}$   | 370 |     | 1800 | mV       | 4 |
| LOS                              | High        |     | 2.0 |      | Vcc      | V |
|                                  | Low         |     |     | 0.8  | V        |   |

**Notes:**

1. The optical power is launched into MMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS  $2^{23}-1$  test pattern @2488Mbps, BER  $\leq 1 \times 10^{-12}$ .
4. Internally AC-coupled.

**Timing and Electrical**

| Parameter                                       | Symbol              | Min | Typical | Max | Unit    |
|---|---------------------|-----|---------|-----|---------|
| Tx Disable Negate Time                          | $t_{on}$            |     |         | 1   | ms      |
| Tx Disable Assert Time                          | $t_{off}$           |     |         | 10  | $\mu s$ |
| Time To Initialize, including Reset of Tx Fault | $t_{init}$          |     |         | 300 | ms      |
| Tx Fault Assert Time                            | $t_{fault}$         |     |         | 100 | $\mu s$ |
| Tx Disable To Reset                             | $t_{reset}$         | 10  |         |     | $\mu s$ |
| LOS Assert Time                                 | $t_{loss_{on}}$     |     |         | 100 | $\mu s$ |
| LOS De-assert Time                              | $t_{loss_{off}}$    |     |         | 100 | $\mu s$ |
| Serial ID Clock Rate                            | $f_{serial\_clock}$ |     |         | 400 | KHz     |
| MOD_DEF (0:2)-High                              | $V_H$               | 2   |         | Vcc | V       |
| MOD_DEF (0:2)-Low                               | $V_L$               |     |         | 0.8 | V       |

## Diagnostics

| Parameter    | Range      | Unit | Accuracy | Calibration         |
|--------------|------------|------|----------|---------------------|
| Temperature  | 0 to +70   | °C   | ±3°C     | Internal / External |
|              | -40 to +85 |      |          |                     |
| Voltage      | 3.0 to 3.6 | V    | ±3%      | Internal / External |
| Bias Current | 0 to 100   | mA   | ±10%     | Internal / External |
| TX Power     | -10 to -3  | dBm  | ±3dB     | Internal / External |
| RX Power     | -22 to -3  | dBm  | ±3dB     | Internal / External |

## Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

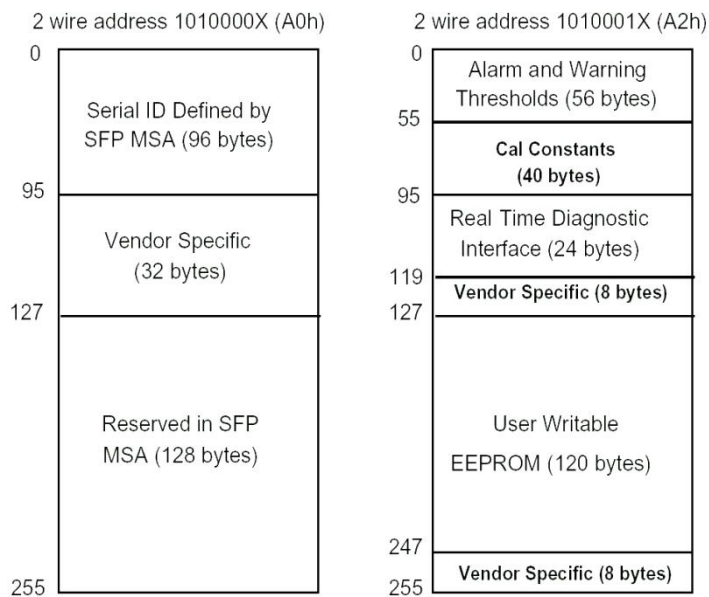


Figure2. Digital Diagnostic Memory Map

## Pin Definitions

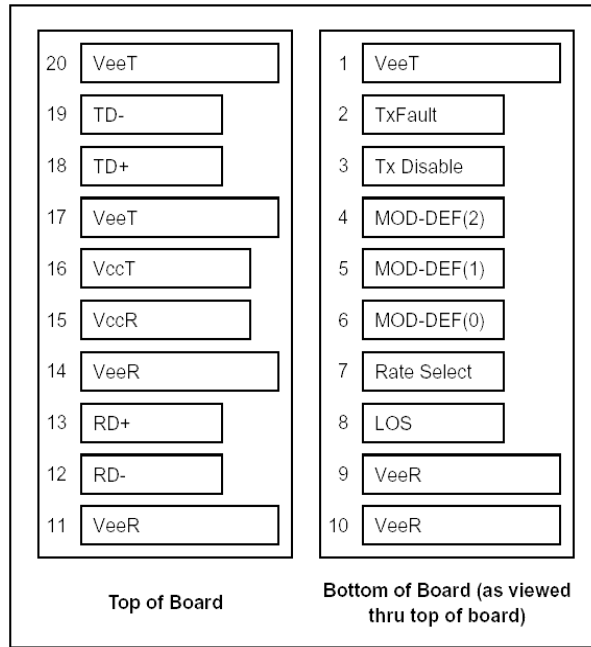


Figure3. Pin Diagram

## Pin Descriptions

| Pin | Signal Name      | Description                  | Plug Seq. | Notes  |
|-----|------------------|------------------------------|-----------|--------|
| 1   | V <sub>EET</sub> | Transmitter Ground           | 1         |        |
| 2   | TX FAULT         | Transmitter Fault Indication | 3         | Note 1 |
| 3   | TX DISABLE       | Transmitter Disable          | 3         | Note 2 |
| 4   | MOD_DEF(2)       | SDA Serial Data Signal       | 3         | Note 3 |
| 5   | MOD_DEF(1)       | SCL Serial Clock Signal      | 3         | Note 3 |
| 6   | MOD_DEF(0)       | TTL Low                      | 3         | Note 3 |
| 7   | Rate Select      | Not Connected                | 3         |        |
| 8   | LOS              | Loss of Signal               | 3         | Note 4 |
| 9   | V <sub>EER</sub> | Receiver ground              | 1         |        |
| 10  | V <sub>EER</sub> | Receiver ground              | 1         |        |
| 11  | V <sub>EER</sub> | Receiver ground              | 1         |        |
| 12  | RD-              | Inv. Received Data Out       | 3         | Note 5 |
| 13  | RD+              | Received Data Out            | 3         | Note 5 |
| 14  | V <sub>EER</sub> | Receiver ground              | 1         |        |
| 15  | V <sub>CCR</sub> | Receiver Power Supply        | 2         |        |
| 16  | V <sub>CCT</sub> | Transmitter Power Supply     | 2         |        |

Datasheet

|    |                  |                       |   |        |
|----|------------------|-----------------------|---|--------|
| 17 | V <sub>EET</sub> | Transmitter Ground    | 1 |        |
| 18 | TD+              | Transmit Data In      | 3 | Note 6 |
| 19 | TD-              | Inv. Transmit Data In | 3 | Note 6 |
| 20 | V <sub>EET</sub> | Transmitter Ground    | 1 |        |

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and V<sub>cc</sub>+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:  
 Low (0 to 0.8V) : Transmitter on  
 (>0.8V, < 2.0V) : Undefined  
 High (2.0 to 3.465V) : Transmitter Disabled  
 Open : Transmitter Disabled
- Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board. The pull-up voltage shall be V<sub>ccT</sub> or V<sub>ccR</sub>.  
 Mod-Def 0 is grounded by the module to indicate that the module is present  
 Mod-Def 1 is the clock line of two wire serial interface for serial ID  
 Mod-Def 2 is the data line of two wire serial interface for serial ID
- LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and V<sub>cc</sub>+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit

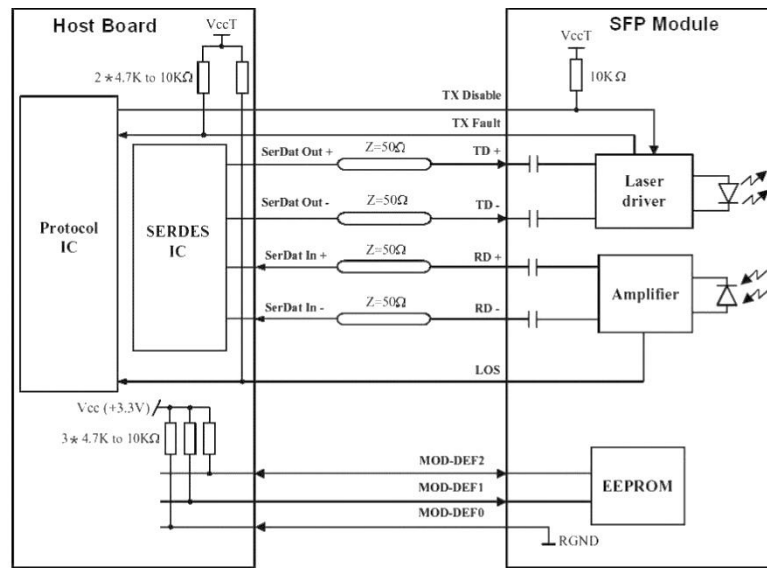


Figure4. Interface Circuit

## Mechanical Dimensions

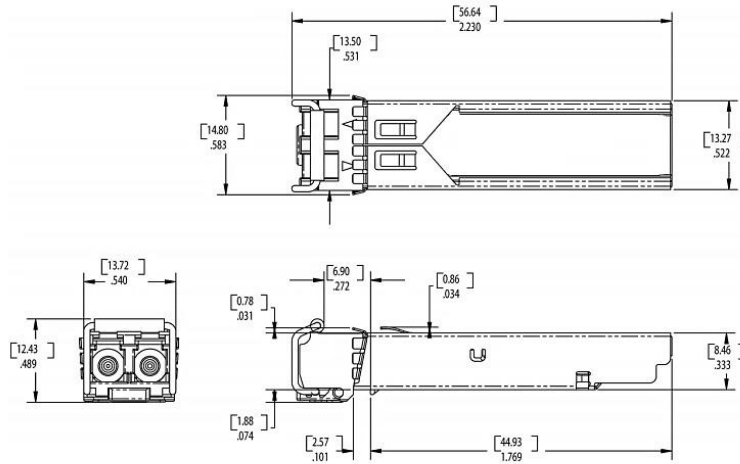


Figure5. Mechanical Dimensions

## References

1. Small Form Factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 2000.
2. Telcordia GR-253-CORE and ITU-T G.957 Specifications.

### Shenzhen Sourcelight Technology Co., Ltd

Sourcelight Technology reserves the right to make changes to or discontinue any optical link product or service identified in this document without notice in order to improve design and/or performance. If you have any question regarding this specification sheet, please contact our sales representative or send email to [sales@sourcelight.com.cn](mailto:sales@sourcelight.com.cn)